

Circuitos Integrados Monolíticos

Analógicos

Amplificadores Operacionales

Reguladores de Tensión

Multiplicadores

Lazos de Enlace de Fase (PLL)

Conversores

D/A

A/D

# Circuitos Integrados

Digitales

Familias Lógicas

TTL

ECL

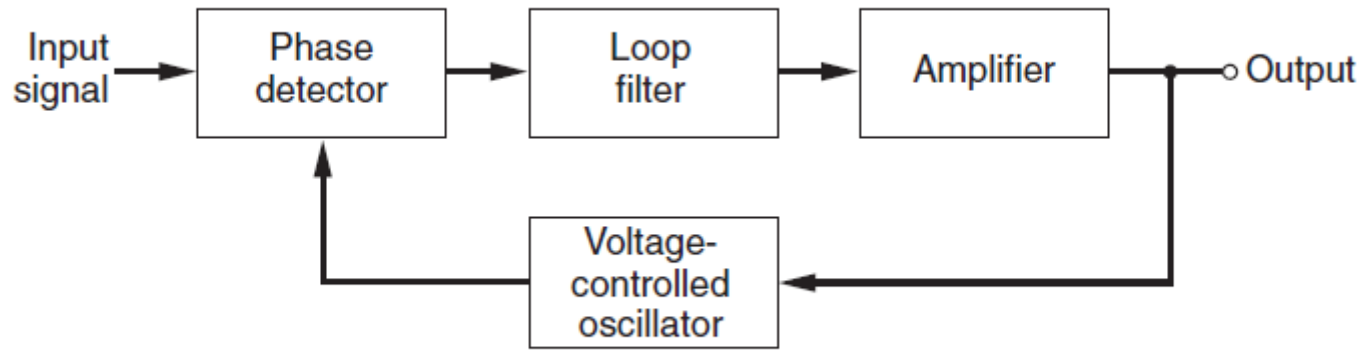
NMOS

CMOS

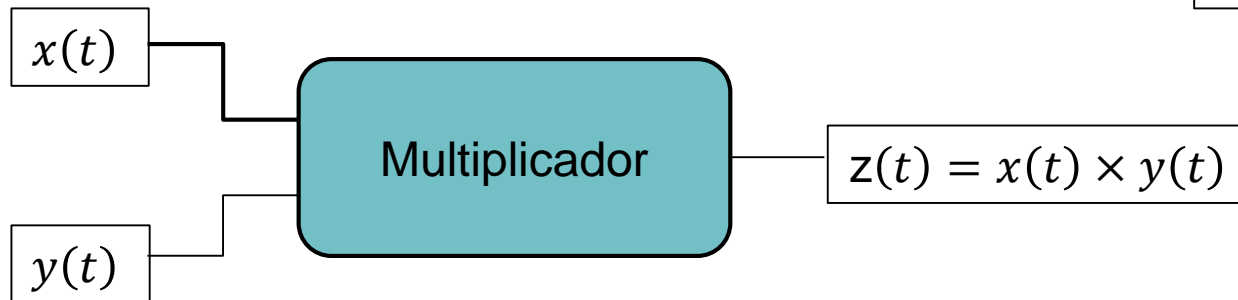
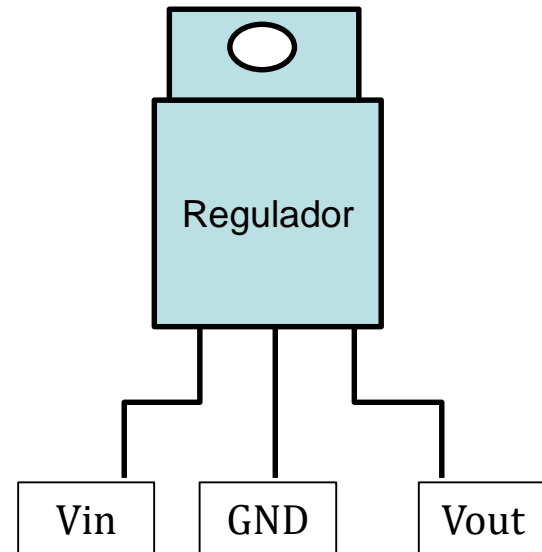
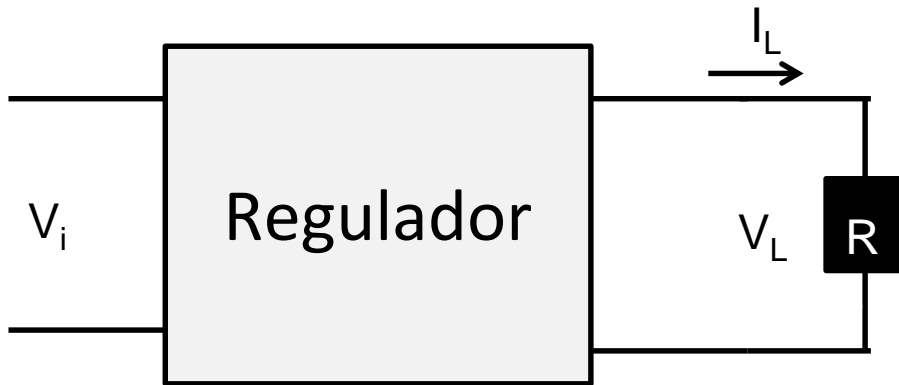
Memorias

$\mu$ P

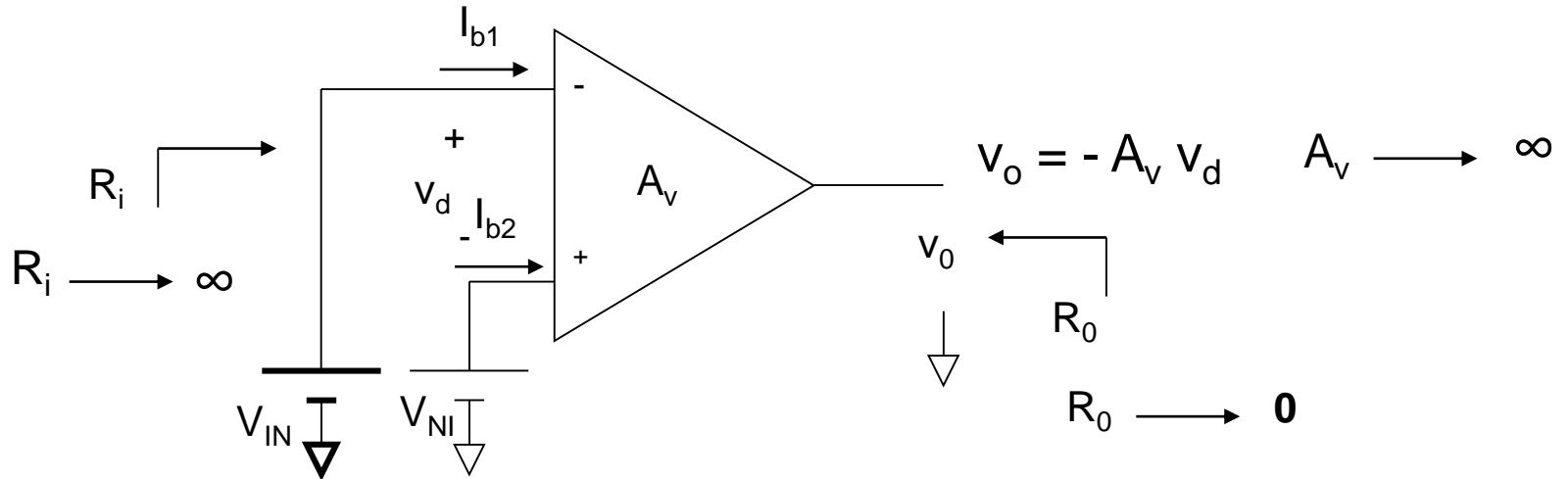
$\mu$ -Controladores



Phase-locked-loop system.



# AMPLIFICADOR OPERACIONAL IDEAL



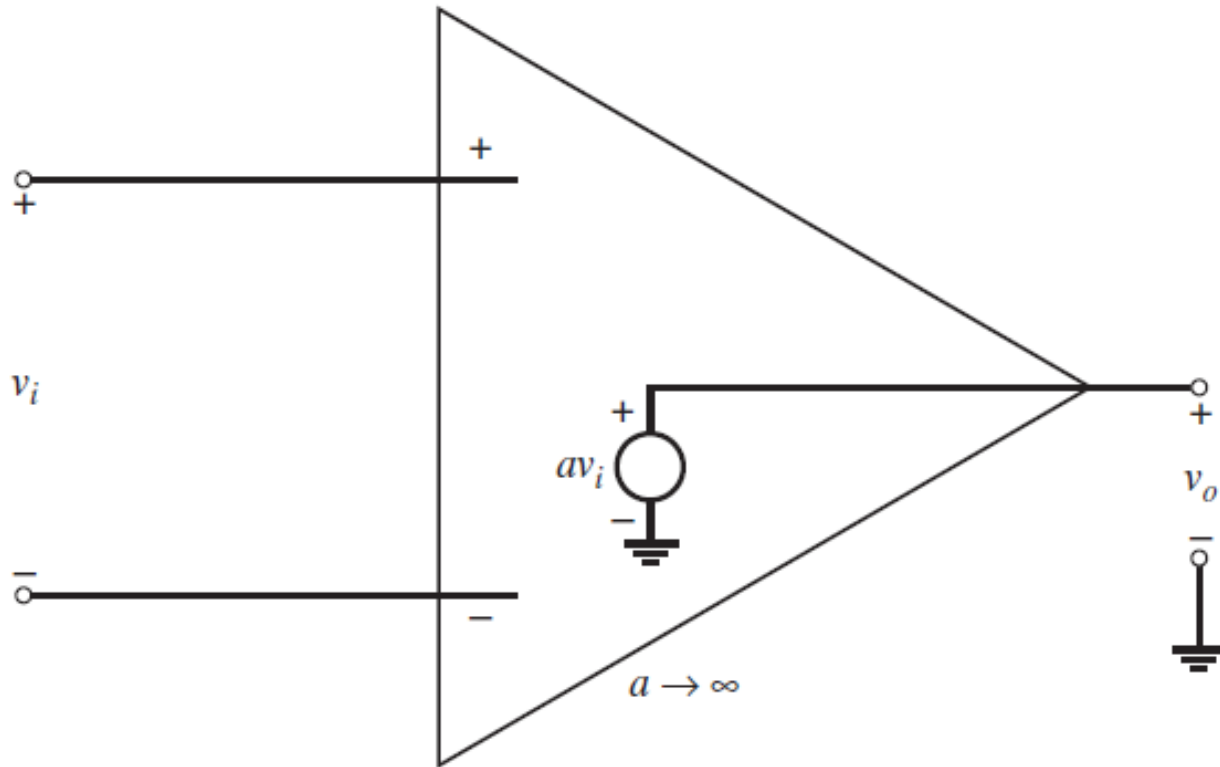
$$V_{IN} = V_{NI} = 0 \rightarrow v_0 = 0$$

$$I_{b1} = I_{b2} = 0$$

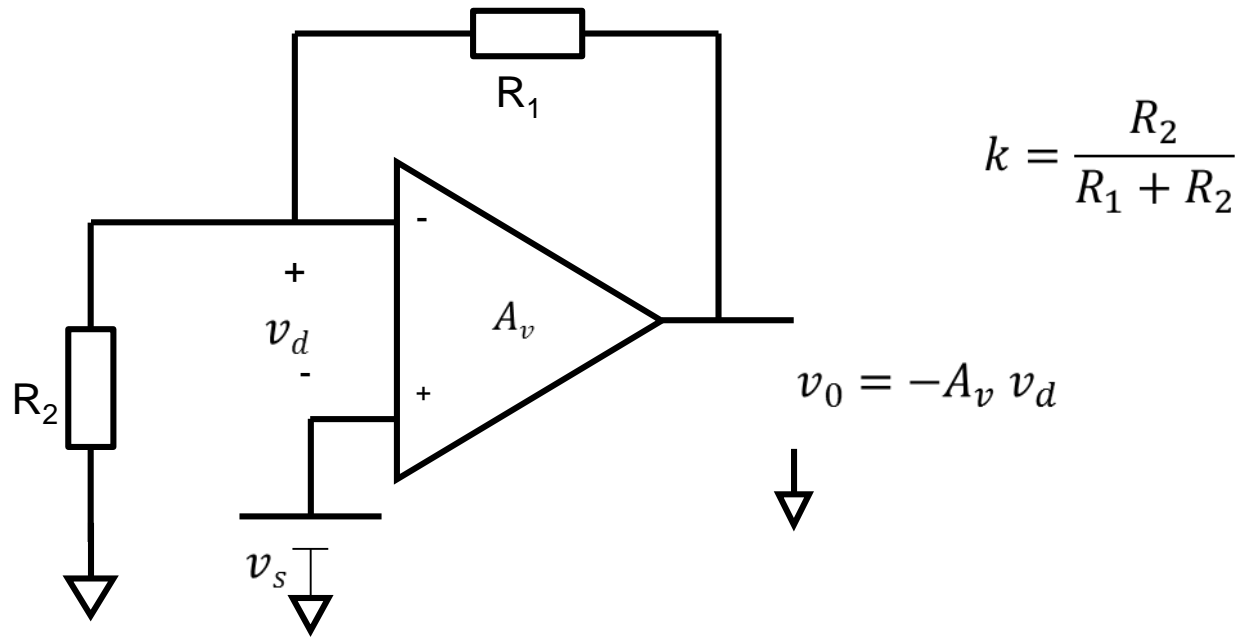
$$A_v \rightarrow \infty$$

- Sin limite en los valores de las tensiones
- Sin limite de Potencia
- Parámetros independientes de Temperatura
- Como  $A_v \rightarrow \infty$  cualquier valor de  $v_d \neq 0$  provoca una indeterminación en el valor de  $v_0$

# MODELO DEL AMPLIFICADOR OPERACIONAL IDEAL



# Amplificador No Inversor

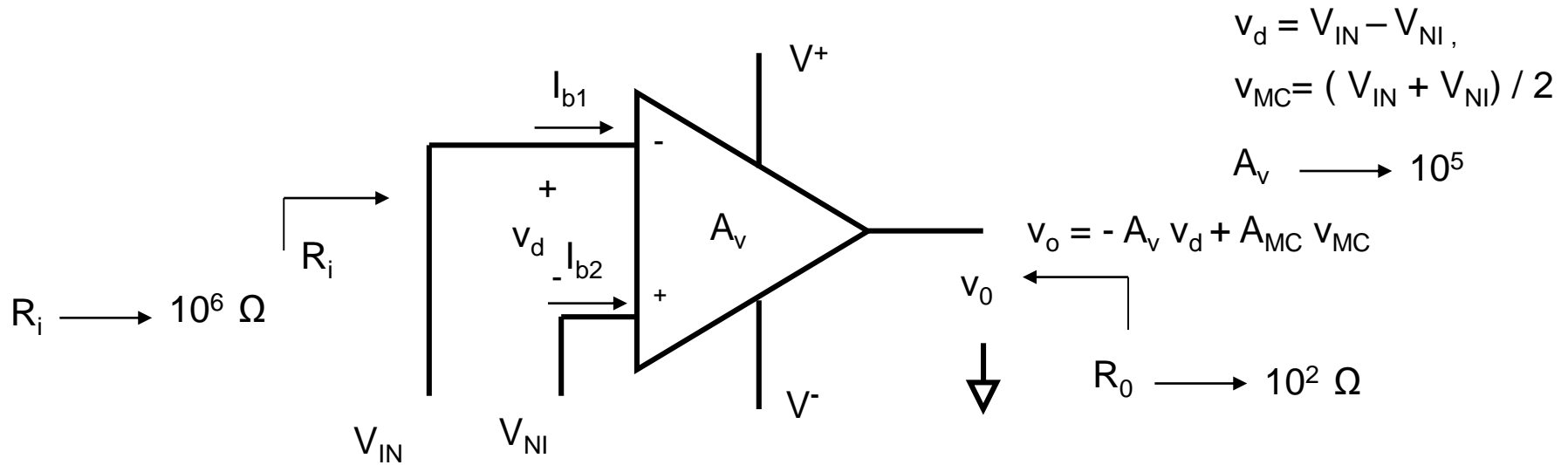


$$v_d = v_{R_2} - v_s \quad v_0 = -A_v (v_{R_2} - v_s) \quad v_{R_2} = \frac{v_0 R_2}{R_1 + R_2} \quad v_0 = -A_v \frac{v_0 R_2}{R_1 + R_2} + A_v v_s$$

$$v_0 \left( 1 + A_v \frac{R_2}{R_1 + R_2} \right) = A_v v_s \quad v_0 (1 + A_v k) = A_v v_s \quad \frac{v_0}{v_s} = A_R = \frac{A_v}{(1 + k A_v)}$$

$$A_R = \frac{1}{\left( \frac{1}{A_v} + k \right)} \quad A_v \rightarrow \infty \quad A_R = \frac{1}{k} \quad A_R = 1 + \frac{R_1}{R_2}$$

# AMPLIFICADOR OPERACIONAL REAL



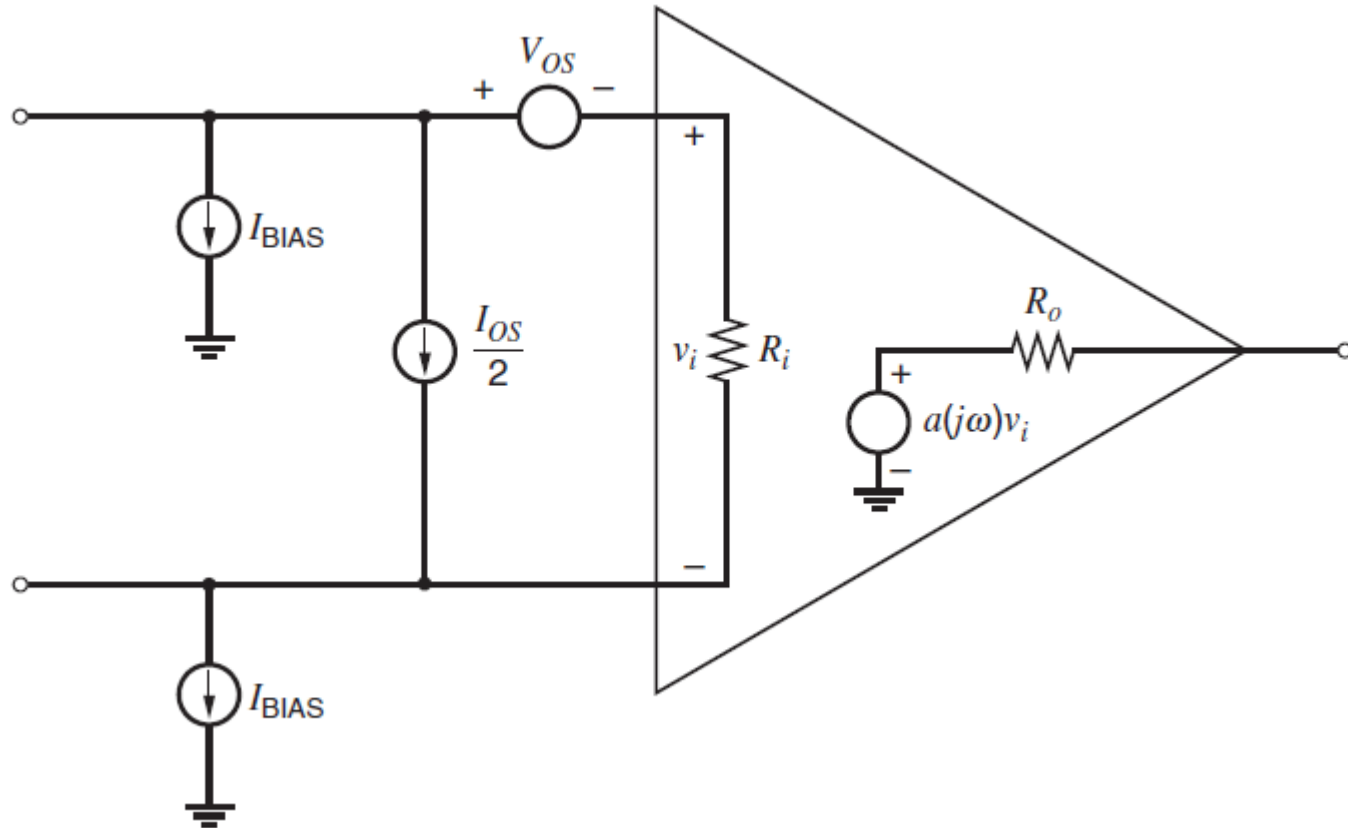
$V_{IN} = V_{NI} = 0 \rightarrow v_o \neq 0 \rightarrow V_{io}$ : Offset de tensión

$I_{b1} \neq I_{b2} \neq 0 \rightarrow I_{io}$ : Offset de corriente

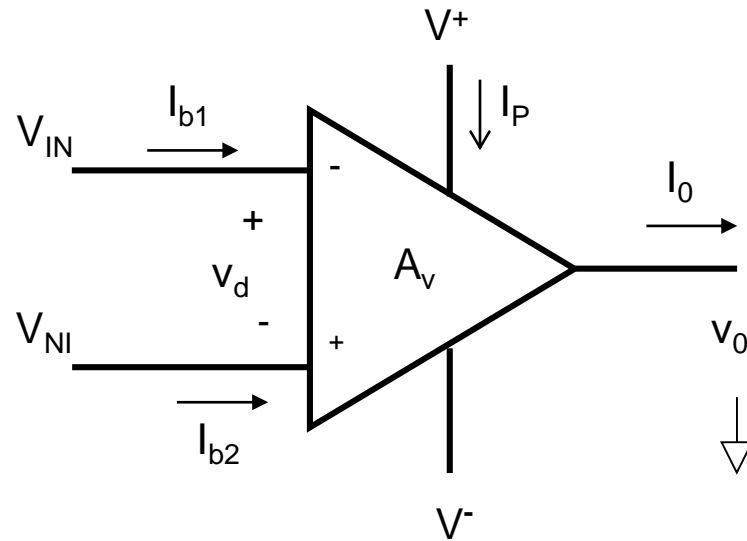
AB  $\rightarrow 10^6$  Hz

# Modelo del AMPLIFICADOR OPERACIONAL

- Off-set de tensión  $V_{OS}$
- Off-set de corriente  $I_{OS}$
- Corriente de polarización de entrada  $I_{BIAS}$
- Resistencia de entrada  $R_i$
- Resistencia de salida  $R_o$



# ESPECIFICACIONES DE LOS AMP. OP.



## Máximos Absolutos

- $V^+$  y  $V^-$
- $P_M$
- $v_{dMAX}$
- $V_{INMAX}$  y  $V_{NI MAX}$
- $I_{0MAX}$  o máxima duración del cortocircuito de salida
- $T_{jMAX}$



# MAXIMOS ABSOLUTOS XX741

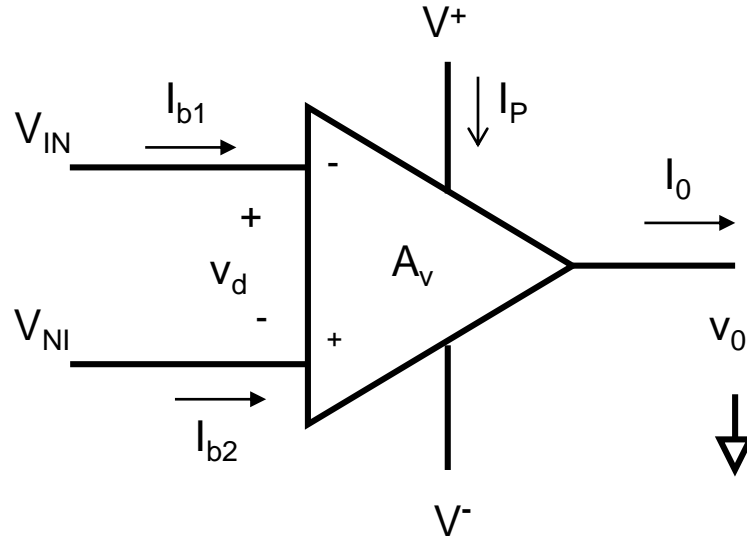
## Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

(Note 6)

	LM741A	LM741E	LM741	LM741C
Supply Voltage	±22V	±22V	±22V	±18V
Power Dissipation (Note 2)	500 mW	500 mW	500 mW	500 mW
Differential Input Voltage	±30V	±30V	±30V	±30V
Input Voltage (Note 3)	±15V	±15V	±15V	±15V
Output Short Circuit Duration	Continuous	Continuous	Continuous	Continuous
Operating Temperature Range	-55°C to +125°C	0°C to +70°C	-55°C to +125°C	0°C to +70°C
Storage Temperature Range	-65°C to +150°C	-65°C to +150°C	-65°C to +150°C	-65°C to +150°C
Junction Temperature	150°C	100°C	150°C	100°C
Soldering Information				
N-Package (10 seconds)	260°C	260°C	260°C	260°C
J- or H-Package (10 seconds)	300°C	300°C	300°C	300°C
M-Package				
Vapor Phase (60 seconds)	215°C	215°C	215°C	215°C
Infrared (15 seconds)	215°C	215°C	215°C	215°C
See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.				
ESD Tolerance (Note 7)	400V	400V	400V	400V

# ESPECIFICACIONES DE LOS AMP. OP.



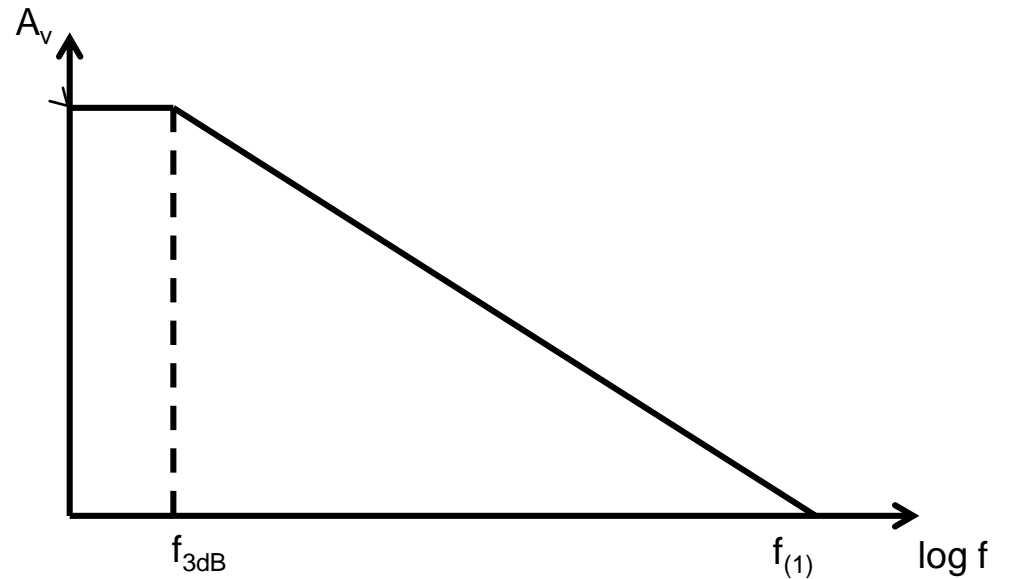
## Características Eléctricas

- $V_{io}$
- $\frac{dV_{io}}{dT}$
- $I_{io} = |I_{b1} - I_{b2}|_{\max}$
- $\frac{dI_{io}}{dT}$
- $I_b$
- $R_i$
- $A_v$
- $R_o$
- Max Excursión de  $v_0$
- $I_{o\max}$
- CMRR
- Relación de Rechazo de fuente
- AB
- $I_P$
- Slew Rate  $(\frac{dv_0}{dt})_{\max}$

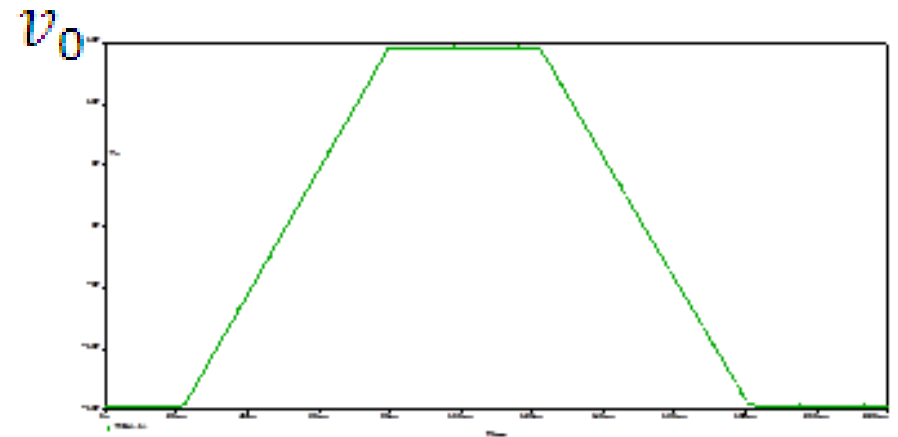
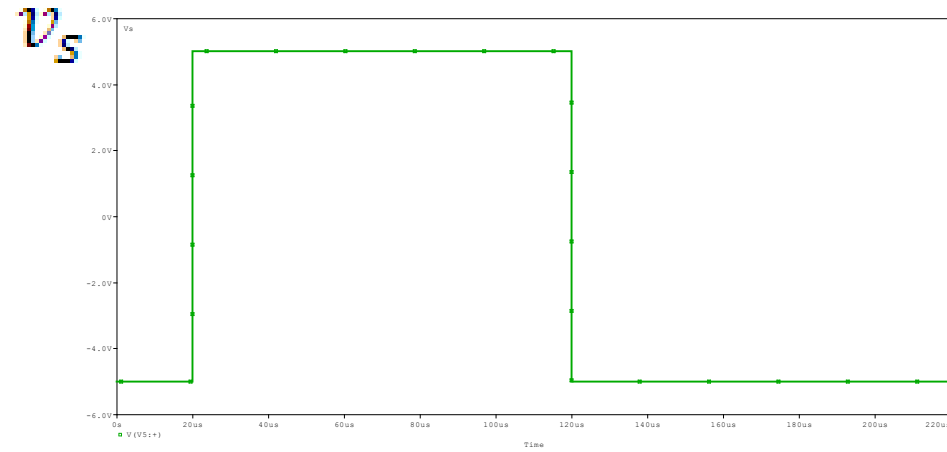
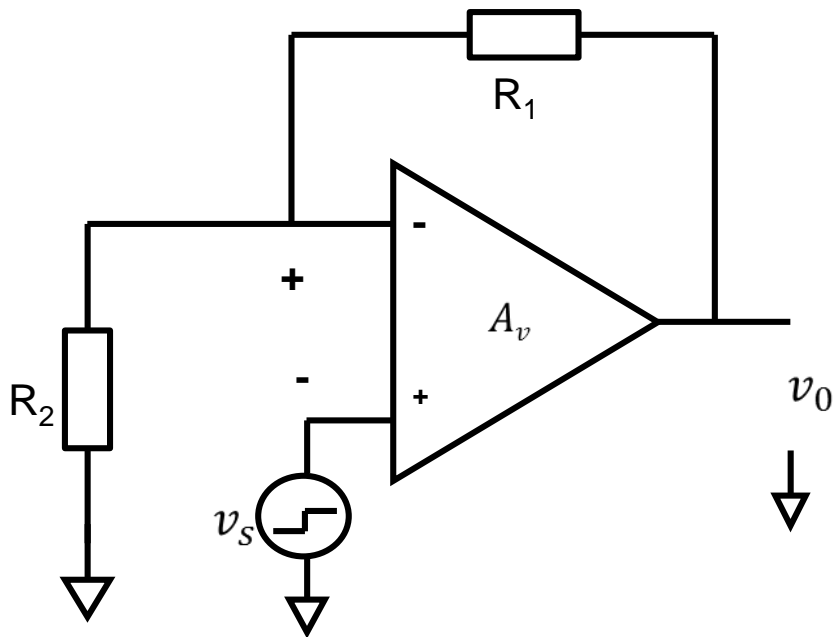
Relación de Rechazo de Modo Común (RRMC)  $\longrightarrow$   $RRMC = 20 \log \frac{A_v}{A_{MC}}$

Relación de Rechazo de Fuente  $\longrightarrow$   $20 \log \frac{\Delta V_0}{\Delta V^+}$      $\circ$      $20 \log \frac{\Delta V_0}{\Delta V^-}$

Ancho de Banda



$$\text{Slew - Rate} = \left( \frac{dv_0}{dt} \right)_{MAX}$$



# CARACTERISTICAS ELECTRICAS DEL XX741

Parameter	Conditions	LM741A/LM741E			LM741			LM741C			Units
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
Input Offset Voltage	$T_A = 25^\circ\text{C}$ $R_S \leq 10\text{ k}\Omega$ $R_S \leq 50\Omega$		0.8	3.0		1.0	5.0		2.0	6.0	mV mV
	$T_{AMIN} \leq T_A \leq T_{AMAX}$ $R_S \leq 50\Omega$ $R_S \leq 10\text{ k}\Omega$			4.0			6.0			7.5	mV mV
Average Input Offset Voltage Drift				15							$\mu\text{V}/^\circ\text{C}$
Input Offset Voltage Adjustment Range	$T_A = 25^\circ\text{C}, V_S = \pm 20\text{V}$	$\pm 10$				$\pm 15$			$\pm 15$		mV
Input Offset Current	$T_A = 25^\circ\text{C}$		3.0	30		20	200		20	200	nA
	$T_{AMIN} \leq T_A \leq T_{AMAX}$			70		85	500			300	nA
Average Input Offset Current Drift				0.5							$\text{nA}/^\circ\text{C}$
Input Bias Current	$T_A = 25^\circ\text{C}$		30	80		80	500		80	500	nA
	$T_{AMIN} \leq T_A \leq T_{AMAX}$			0.210			1.5			0.8	$\mu\text{A}$
Input Resistance	$T_A = 25^\circ\text{C}, V_S = \pm 20\text{V}$	1.0	6.0		0.3	2.0		0.3	2.0		$\text{M}\Omega$
	$T_{AMIN} \leq T_A \leq T_{AMAX},$ $V_S = \pm 20\text{V}$	0.5									$\text{M}\Omega$
Input Voltage Range	$T_A = 25^\circ\text{C}$							$\pm 12$	$\pm 13$		V
	$T_{AMIN} \leq T_A \leq T_{AMAX}$				$\pm 12$	$\pm 13$					V

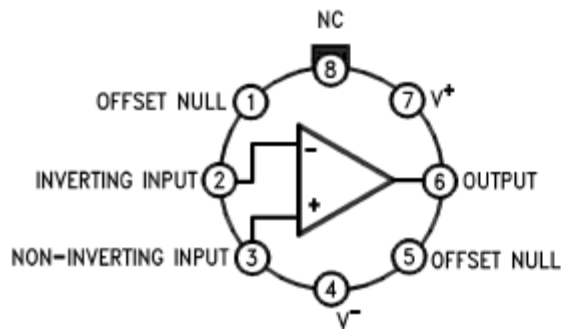
# CARACTERISTICAS ELECTRICAS DEL XX741

Parameter	Conditions	LM741A/LM741E			LM741			LM741C			Units
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
Large Signal Voltage Gain	$T_A = 25^\circ\text{C}$ , $R_L \geq 2\text{ k}\Omega$ $V_S = \pm 20\text{V}$ , $V_O = \pm 15\text{V}$ $V_S = \pm 15\text{V}$ , $V_O = \pm 10\text{V}$	50			50	200		20	200		V/mV V/mV
	$T_{AMIN} \leq T_A \leq T_{AMAX}$ , $R_L \geq 2\text{ k}\Omega$ , $V_S = \pm 20\text{V}$ , $V_O = \pm 15\text{V}$ $V_S = \pm 15\text{V}$ , $V_O = \pm 10\text{V}$	32			25			15			V/mV V/mV
	$V_S = \pm 5\text{V}$ , $V_O = \pm 2\text{V}$	10									V/mV
Output Voltage Swing	$V_S = \pm 20\text{V}$ $R_L \geq 10\text{ k}\Omega$ $R_L \geq 2\text{ k}\Omega$	$\pm 16$ $\pm 15$									V V
	$V_S = \pm 15\text{V}$ $R_L \geq 10\text{ k}\Omega$ $R_L \geq 2\text{ k}\Omega$				$\pm 12$ $\pm 10$	$\pm 14$ $\pm 13$		$\pm 12$ $\pm 10$	$\pm 14$ $\pm 13$		V V
Output Short Circuit Current	$T_A = 25^\circ\text{C}$	10	25	35		25			25		mA
	$T_{AMIN} \leq T_A \leq T_{AMAX}$	10		40							mA
Common-Mode Rejection Ratio	$T_{AMIN} \leq T_A \leq T_{AMAX}$ $R_S \leq 10\text{ k}\Omega$ , $V_{CM} = \pm 12\text{V}$				70	90		70	90		dB
	$R_S \leq 50\Omega$ , $V_{CM} = \pm 12\text{V}$	80	95								dB
Supply Voltage Rejection Ratio	$T_{AMIN} \leq T_A \leq T_{AMAX}$ , $V_S = \pm 20\text{V}$ to $V_S = \pm 5\text{V}$										
	$R_S \leq 50\Omega$ $R_S \leq 10\text{ k}\Omega$	86	96		77	96		77	96		dB dB

# CARACTERISTICAS ELECTRICAS DEL XX741

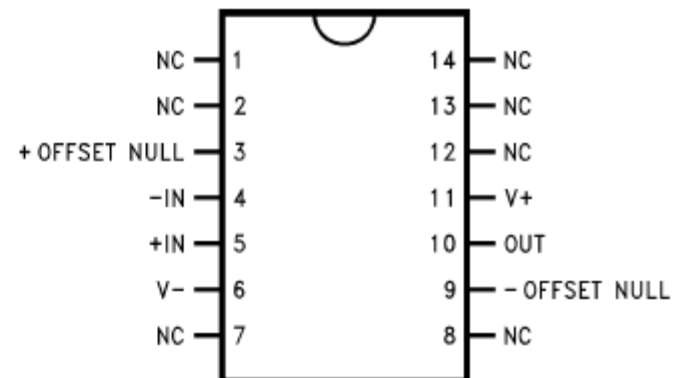
Transient Response	$T_A = 25^\circ\text{C}$ , Unity Gain												
Rise Time			0.25	0.8					0.3		$\mu\text{s}$		
Overshoot			6.0	20					5		%		
Bandwidth (Note 5)	$T_A = 25^\circ\text{C}$	0.437	1.5								MHz		
Slew Rate	$T_A = 25^\circ\text{C}$ , Unity Gain	0.3	0.7						0.5		V/ $\mu\text{s}$		
Supply Current	$T_A = 25^\circ\text{C}$							1.7	2.8		1.7	2.8	mA
Power Consumption	$T_A = 25^\circ\text{C}$												
	$V_S = \pm 20\text{V}$		80	150									mW
	$V_S = \pm 15\text{V}$							50	85		50	85	mW
LM741A	$V_S = \pm 20\text{V}$												
	$T_A = T_{AMIN}$												mW
	$T_A = T_{AMAX}$												mW
LM741E	$V_S = \pm 20\text{V}$												
	$T_A = T_{AMIN}$												mW
	$T_A = T_{AMAX}$												mW
LM741	$V_S = \pm 15\text{V}$												
	$T_A = T_{AMIN}$												mW
	$T_A = T_{AMAX}$							60	100				mW
								45	75				mW

**Metal Can Package**

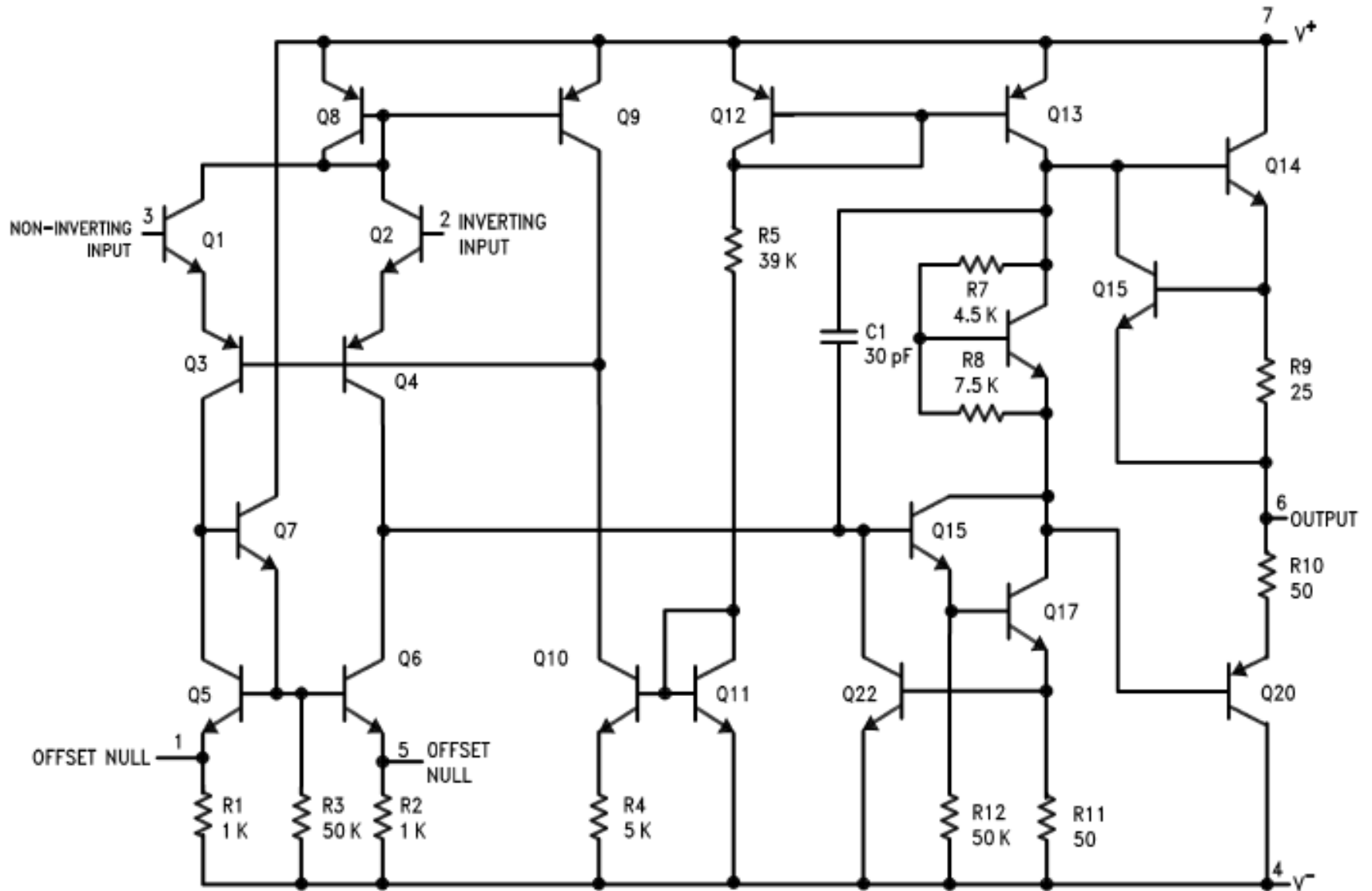


DS009341-2

**Ceramic Dual-In-Line Package**

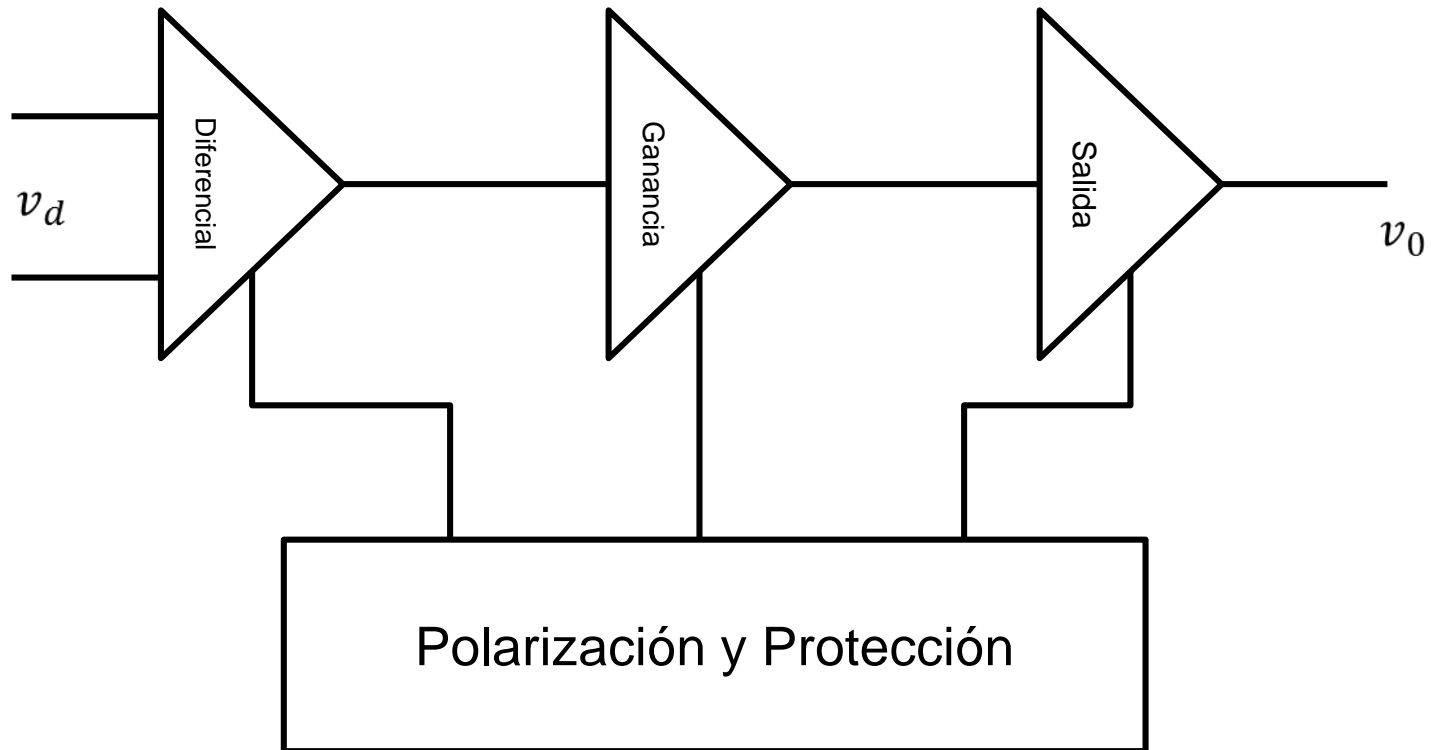


# CIRCUITO INTERNO DEL XX741





# ESQUEMA DEL AMPLIFICADOR OPERACIONAL XX741



- Diferencial
- Q1 – Q2
  - Q3 - Q4
  - Q5 - Q6 – Q7
  - R1 – R2 – R3

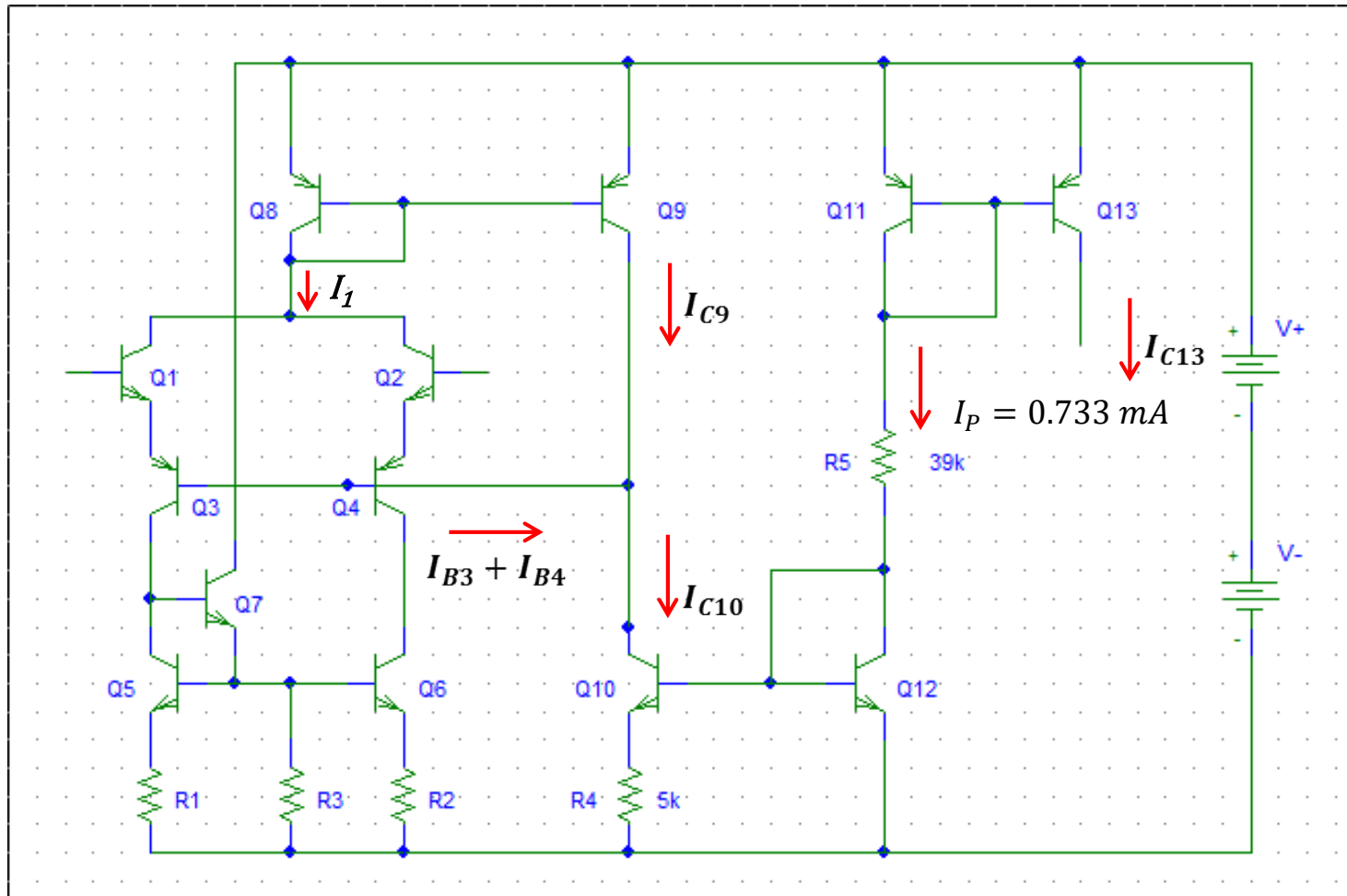
- Ganancia
- Q15 – Q17
  - R11 – R12

- Salida
- Q14 – Q20
  - R10

- Polarización
- Q8 – Q9 – Q10
  - Q11 – Q12 – Q13
  - R4 – R5

- Protección
- Q15 – Q22
  - R9 – R11

# Corrientes de polarización XX741



$$V^+ + V^- - V_{BE11} - V_{R5} - V_{BE12} = 0$$

$$V_{BE11} = V_{BE12} = 0.7V$$

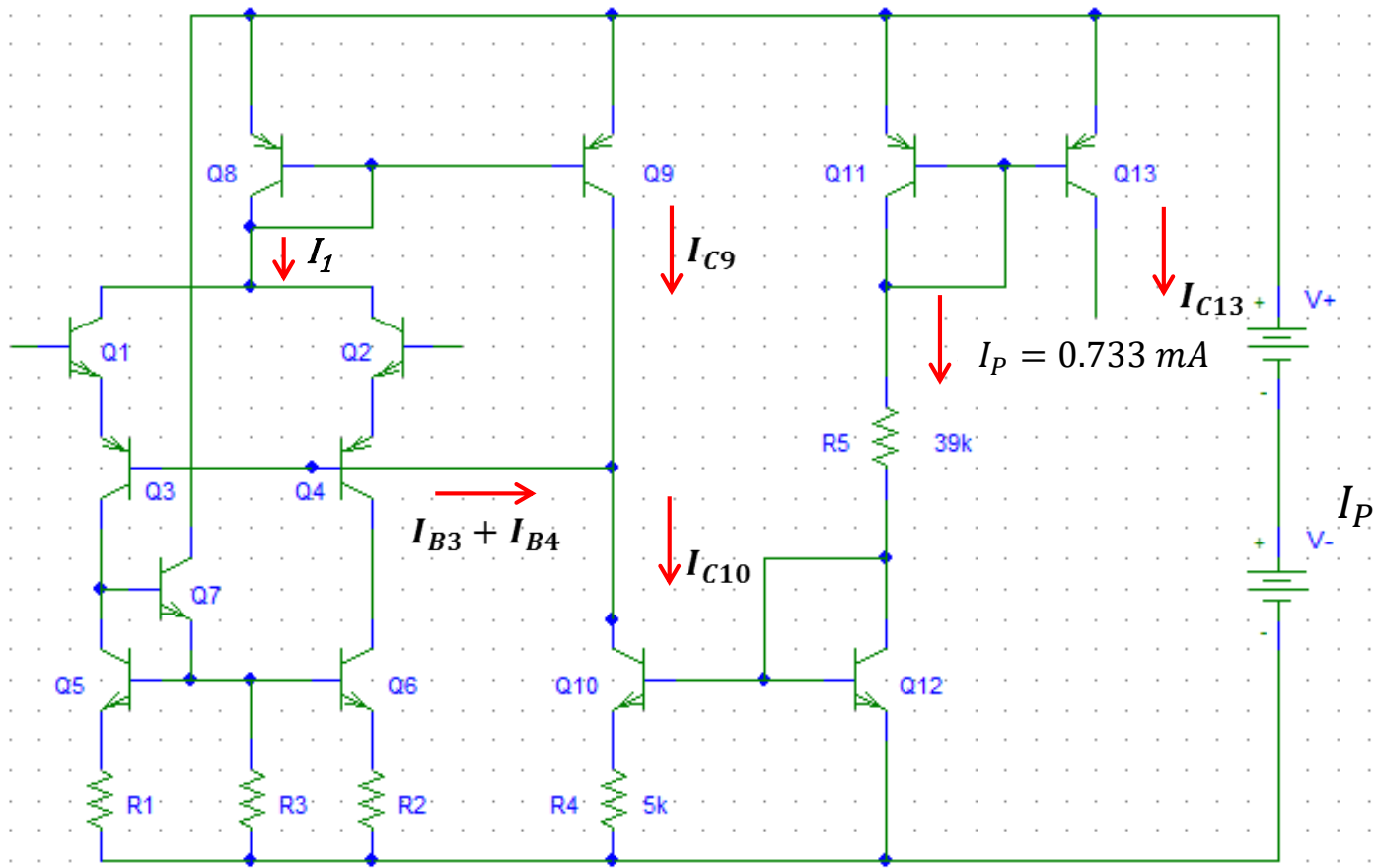
$$V_{R5} = V^+ + V^- - 2V_{BE}$$

$$I_P = \frac{V_{R5}}{R_5}$$

$$I_P = \frac{V^+ + V^- - 2V_{BE}}{R_5}$$

$$\text{Si } V^+ = V^- = 15V$$

$$I_P = \frac{15V + 15V - 2 \times 0.7V}{39K\Omega}$$



$$Q_{11} \equiv Q_{13}$$

$$I_{C13} = \frac{I_P}{\left(1 + \frac{2}{\beta_{PNP}}\right)}$$

$$I_P = I_{C10} \times e^{(I_{C10} \times R_4 / U_T)}$$

$$I_{C10} = 19 \mu A$$

$$I_{C10} = I_{C9} + I_{B3} + I_{B4}$$

$$I_{B3} = I_{B4} = \frac{I_1}{2 \beta_{PNP}}$$

$$I_{C10} = I_{C9} + \frac{I_1}{\beta_{PNP}}$$

$$I_1 = I_{C8} \left(1 + \frac{2}{\beta_{PNP}}\right)$$

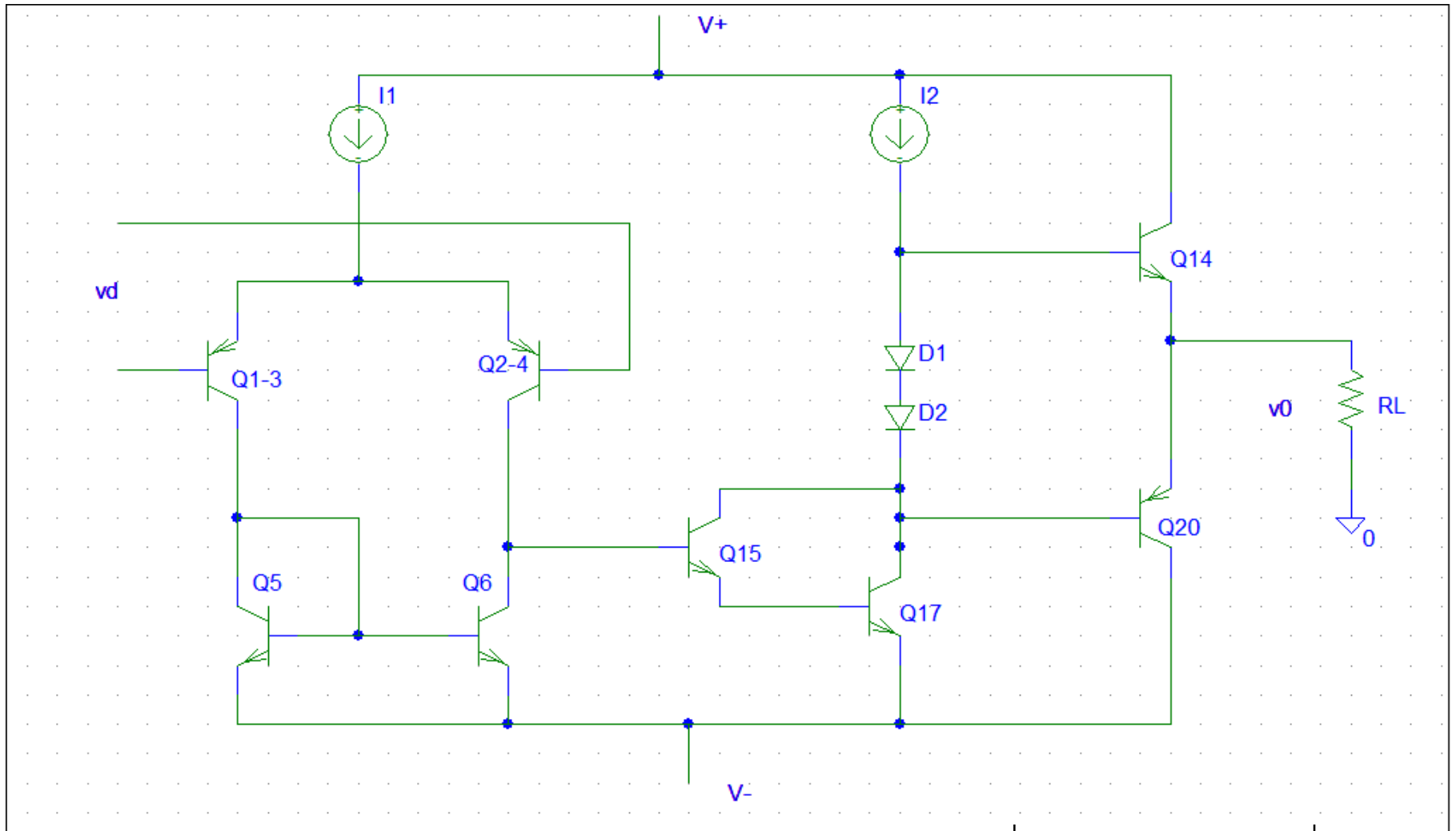
$$I_{C8} = I_{C9}$$

$$I_1 = I_{C9} \left(1 + \frac{2}{\beta_{PNP}}\right)$$

$$I_{C10} = I_1 \left( \frac{1}{1 + \frac{2}{\beta_{PNP}}} + \frac{1}{\beta_{PNP}} \right)$$

↓  
~0.93

# Circuito simplificado para calculo Ri Av AB

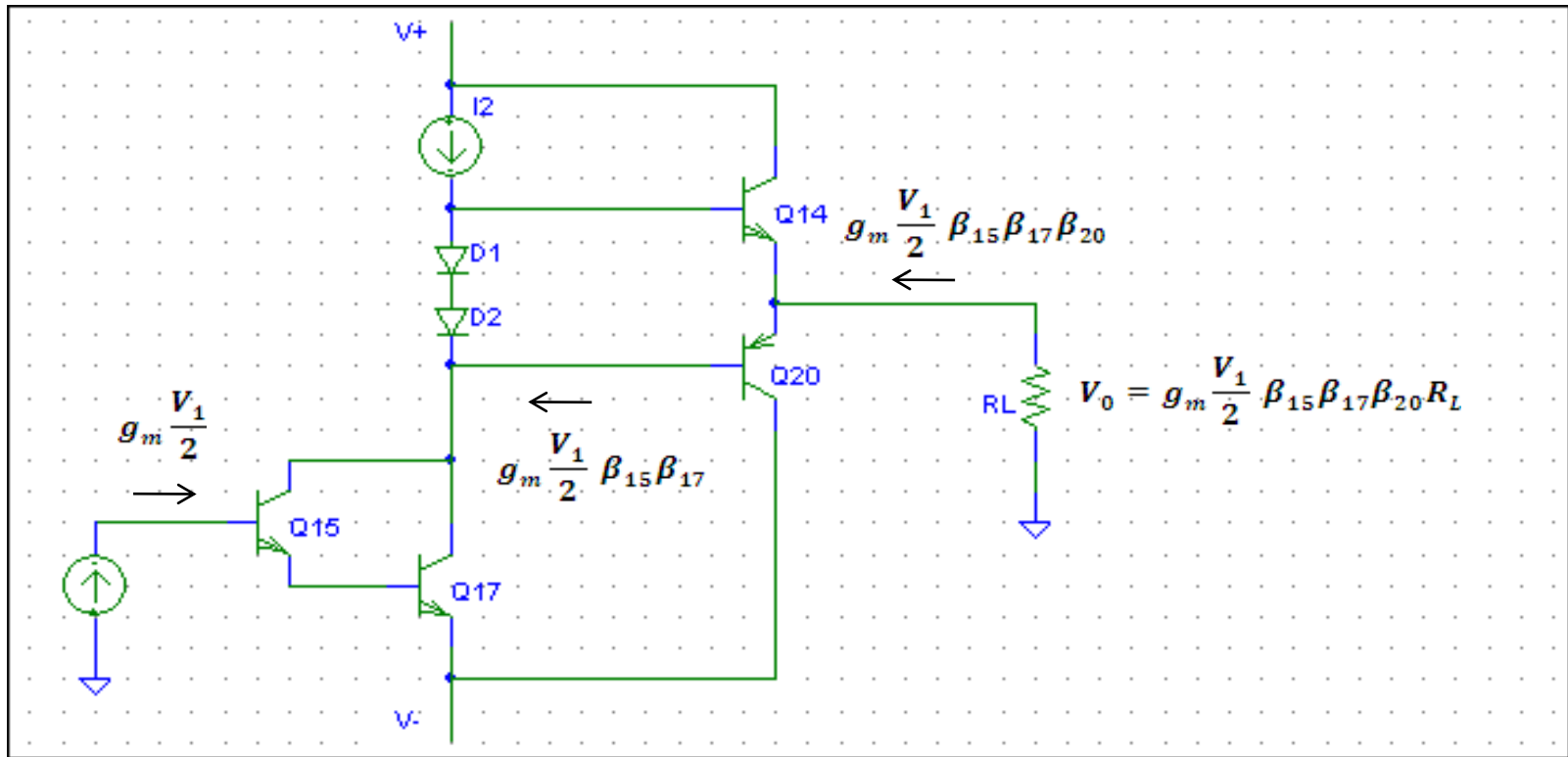


← Etapa de entrada DIFERENCIAL

← Etapa de ganancia DARLINGTON

← Etapa de salida PAR COMPLEMENTARIO

# Ganancia del XX741

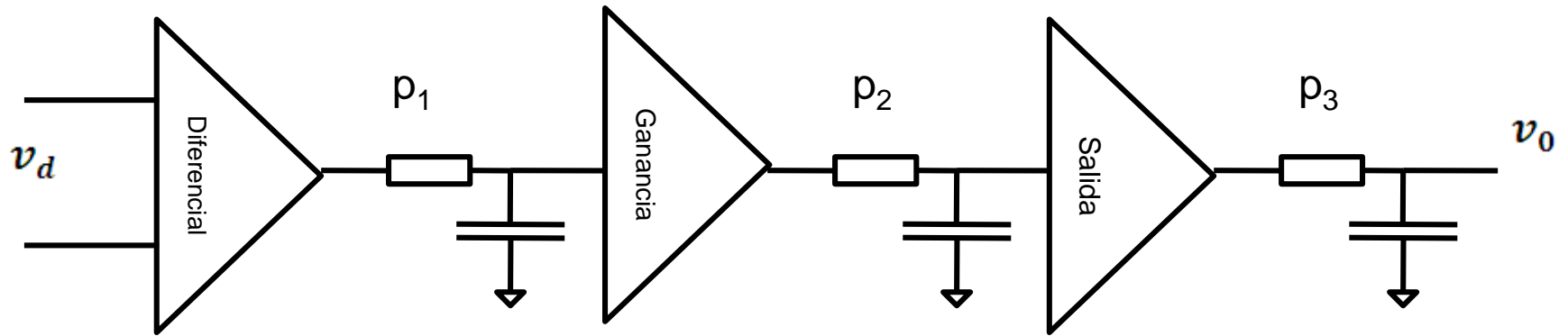


$$A_v = \frac{v_0}{v_1}$$

$$A_v = \frac{g_m}{2} \beta_{15} \beta_{17} \beta_{20} R_L$$

$$A_v = \frac{I_1}{4U_T} \beta_{15} \beta_{17} \beta_{20} R_L$$

# RESPUESTA EN FRECUENCIA DEL XX741



Cada polo atrasa  $90^\circ$

Total atraso polos  $270^\circ$

Entre  $v_d$  y  $v_0$  la fase es  $180^\circ$

Fase total entre  $v_d$  y  $v_0$   $450^\circ$

Si cuando la fase entre  $v_d$  y  $v_0$  es  $360^\circ$   $A_v > 1$   $\Rightarrow$  Inestabilidad

Para hacer estable el circuito  $\Rightarrow$

Cuando la fase entre  $v_d$  y  $v_0$  sea  $360^\circ$  hacer  $A_v < 1$

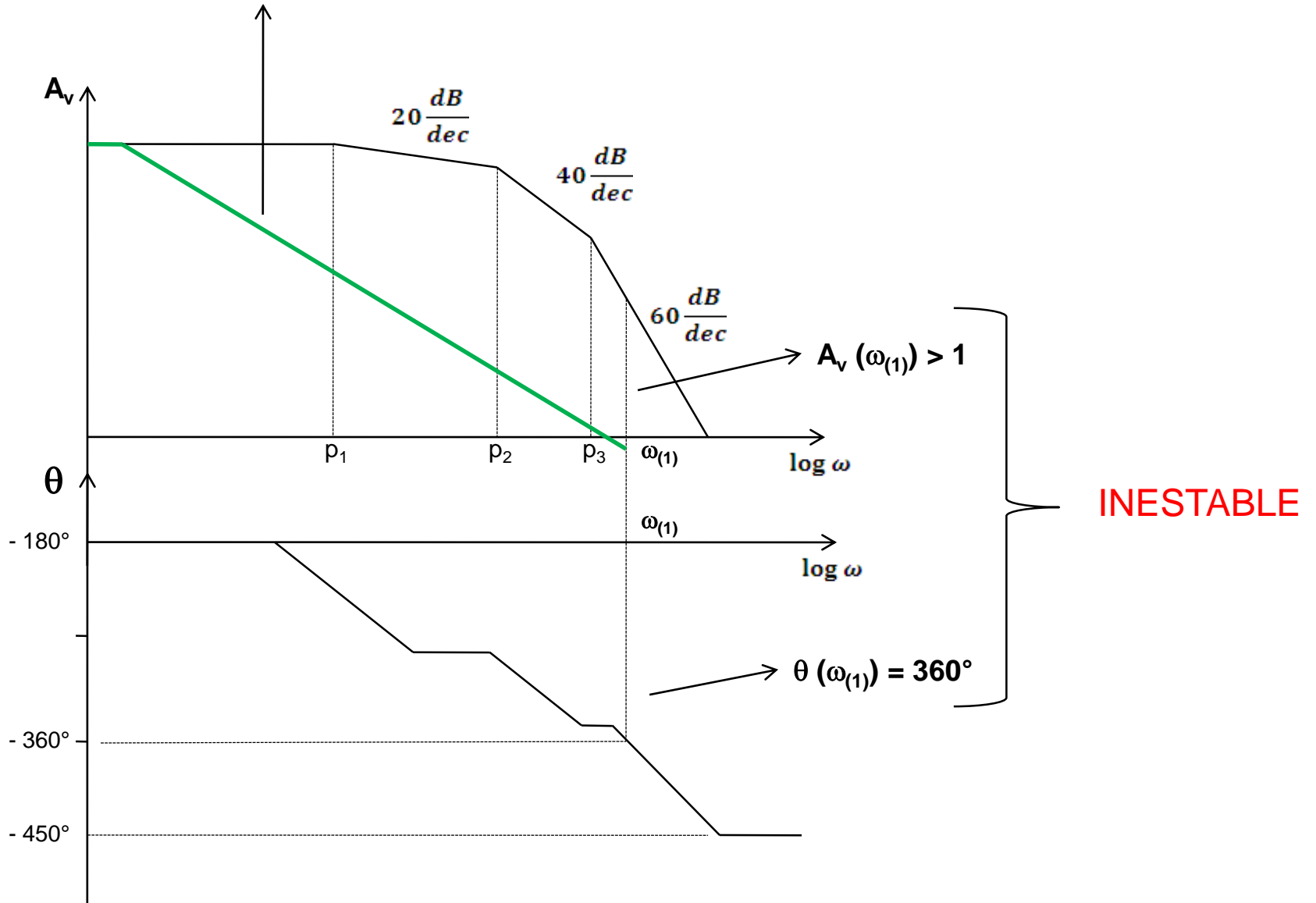
Encontramos la frecuencia  $\omega$  para la cual  $\theta = 360^\circ$

Esta frecuencia es  $\omega_{(1)}$

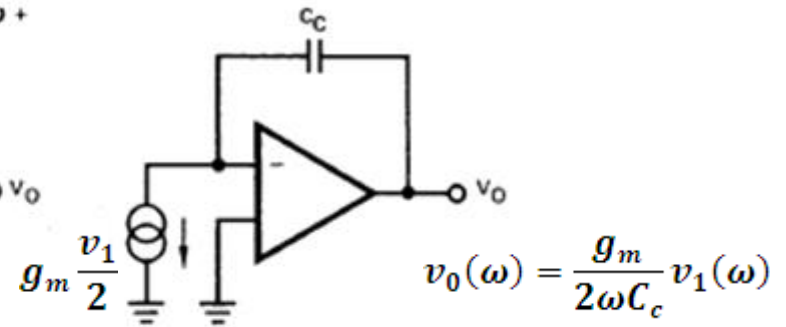
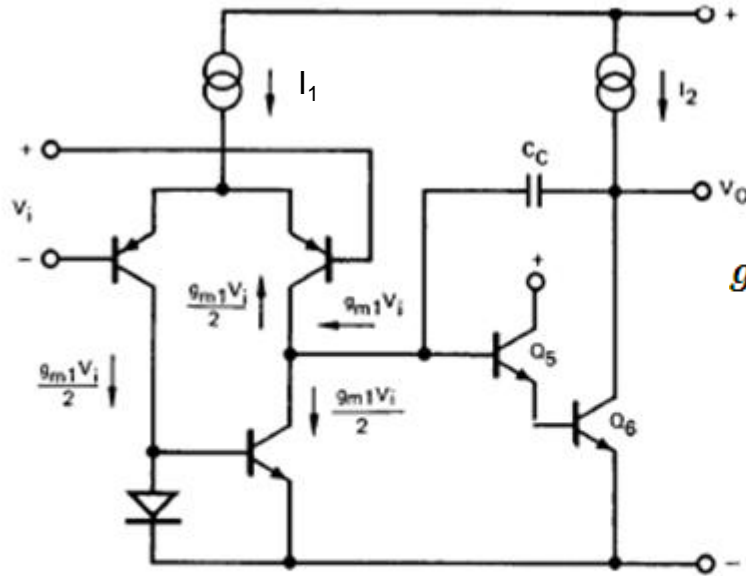
Hacemos que  $A_v(\omega) \leq 1$  para  $\omega = \omega_{(1)}$

# Respuesta en Frecuencia

$A_v(\omega_{(1)}) < 1$   ESTABLE



# COMPENSACION DEL AMP. OP. XX741

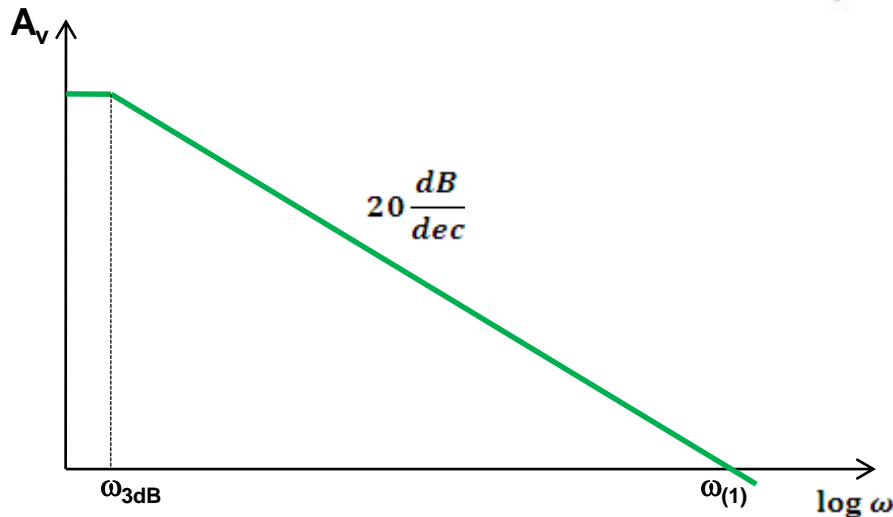


$$A_v(\omega) = \frac{v_0(\omega)}{v_1(\omega)}$$

$$A_v(\omega) = \frac{g_m}{2\omega C_c}$$

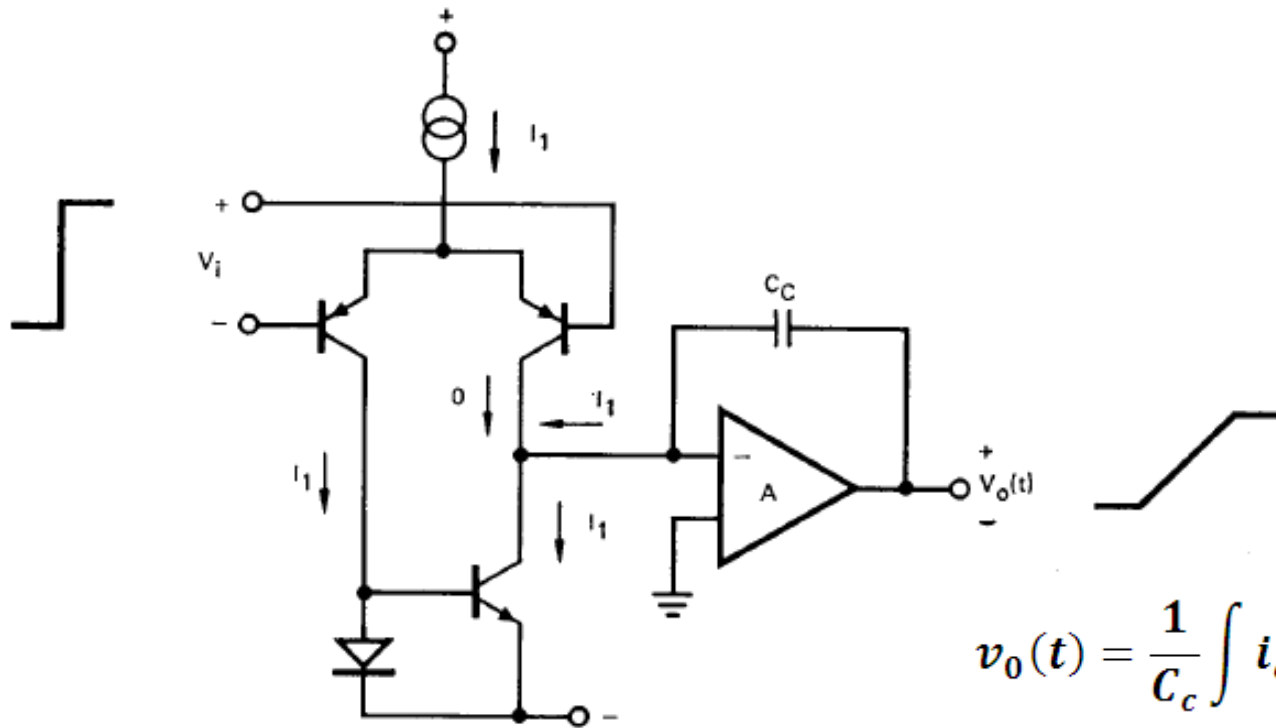
$$A_v(\omega_{(1)}) = \frac{g_m}{2\omega_{(1)} C_c} = 1$$

$$C_c = \frac{g_m}{2\omega_{(1)}} \quad g_m = \frac{I_1}{2U_T}$$





# Slew Rate



$$v_o(t) = \frac{1}{C_c} \int I_1 dt$$

$$v_o(t) = \frac{I_1}{C_c} t$$

$$\left( \frac{dv_o(t)}{dt} \right)_{max} = \frac{I_1}{C_c}$$

$$S - R = \frac{I_1}{C_c}$$

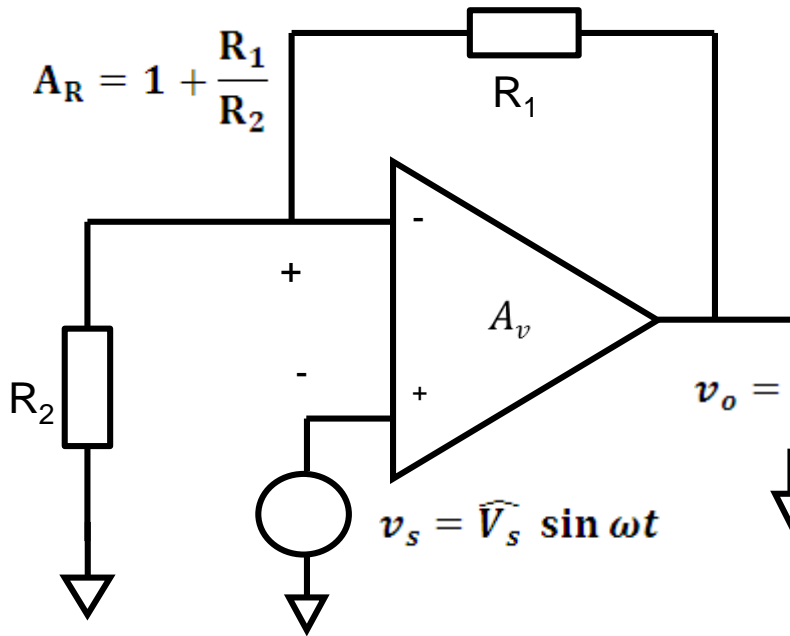
$$\left( \frac{g_m v_1}{2} \right)_{MAX} = I_1$$

$$(v_1)_{MAX} = \frac{2I_1}{g_m}$$

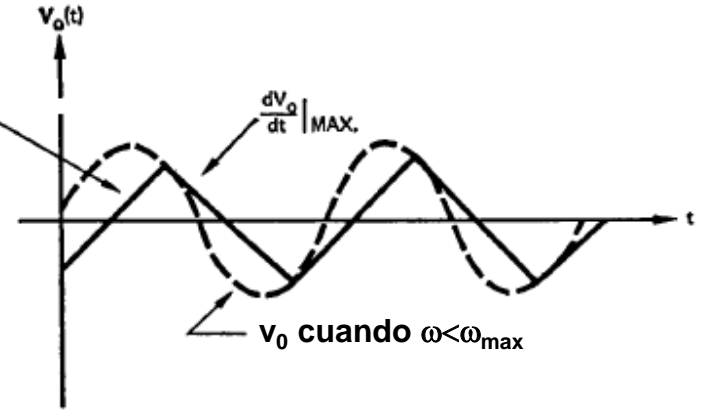
$$g_m = \frac{I_1}{2U_T}$$

$$(v_1)_{MAX} = 4 U_T$$

# Ancho de banda de potencia



$v_o$  cuando  $\omega > \omega_{max}$



$$v_o = A_R \widehat{V}_s \sin \omega t$$

$$v_s = \widehat{V}_s \sin \omega t$$

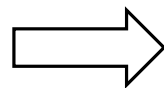
$$\frac{dv_o}{dt} = A_R \widehat{V}_s \omega \cos \omega t$$

Cuando  $\cos \omega t = 1$

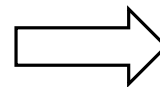
$$\left(\frac{dv_o}{dt}\right)_{max} = A_R \widehat{V}_s \omega$$

Onda senoidal máxima pendiente en el cruce por cero (punto de inflexión)

$$\left(\frac{dv_o}{dt}\right)_{MAX} = S-R$$



$$A_R \widehat{V}_s \omega \leq S-R$$



$$\omega_{max} \leq \frac{S-R}{A_R \widehat{V}_s}$$

# Limite del Slew - Rate

$$S - R = \frac{I_1}{C_c} \quad C_c = \frac{g_m}{2\omega_{(1)}} \quad S - R = \frac{I_1}{g_m/2\omega_{(1)}} \quad S - R = 2\omega_{(1)} \frac{I_1}{g_m}$$

Para mejorar el S-R:

$$\omega_{(1)} \uparrow \quad I_1/g_m \uparrow$$

Cuando la etapa de entrada se implementa con TBJ's

$$g_m = \frac{I_1}{2U_T} \quad \frac{I_1}{g_m} = 2U_T = \text{cte.} \quad S - R = 4U_T\omega_{(1)}$$

Cuando la etapa de entrada se implementa con JFET

$$I_1 = I_{DSS} \left(1 - \frac{V_{GS}}{V_P}\right)^2 \quad g_m = 2 \frac{I_{DSS}}{V_P} \left(1 - \frac{V_{GS}}{V_P}\right) \quad \frac{I_1}{g_m} = \frac{(V_P - V_{GS})}{2}$$

$$\left(\frac{I_1}{g_m}\right)_{max} = \frac{V_P}{2} \quad (I_1)_{max} = I_{DSS} \rightarrow \text{cuando } V_{GS} = 0 \quad S - R = V_P\omega_{(1)}$$

Relacion del S-R de entrada JFET a entrada TBJ

$$\frac{S - R_{(JFET)}}{S - R_{(TBJ)}} = \frac{V_P}{4U_T}$$

# CARACTERISTICAS ELECTRICAS AMP-OP-LF411

## DC Electrical Characteristics (Note 4)

Symbol	Parameter	Conditions	LF411A			LF411			Units
			Min	Typ	Max	Min	Typ	Max	
$V_{OS}$	Input Offset Voltage	$R_S = 10\text{ k}\Omega$ , $T_A = 25^\circ\text{C}$		0.3	0.5		0.8	2.0	mV
$\Delta V_{OS}/\Delta T$	Average TC of Input Offset Voltage	$R_S = 10\text{ k}\Omega$ (Note 5)		7	10		7	20 (Note 5)	$\mu\text{V}/^\circ\text{C}$
$I_{OS}$	Input Offset Current	$V_S = \pm 15\text{V}$ (Notes 4, 6)	$T_j = 25^\circ\text{C}$	25	100		25	100	$\mu\text{A}$
			$T_j = 70^\circ\text{C}$			2		2	nA
			$T_j = 125^\circ\text{C}$			25		25	nA
$I_B$	Input Bias Current	$V_S = \pm 15\text{V}$ (Notes 4, 6)	$T_j = 25^\circ\text{C}$	50	200		50	200	$\mu\text{A}$
			$T_j = 70^\circ\text{C}$			4		4	nA
			$T_j = 125^\circ\text{C}$			50		50	nA
$R_{IN}$	Input Resistance	$T_j = 25^\circ\text{C}$		$10^{12}$			$10^{12}$	$\Omega$	
$A_{VOL}$	Large Signal Voltage Gain	$V_S = \pm 15\text{V}$ , $V_O = \pm 10\text{V}$ , $R_L = 2\text{k}$ , $T_A = 25^\circ\text{C}$	50	200		25	200		V/mV
		Over Temperature	25	200		15	200		V/mV
$V_O$	Output Voltage Swing	$V_S = \pm 15\text{V}$ , $R_L = 10\text{k}$	$\pm 12$	$\pm 13.5$		$\pm 12$	$\pm 13.5$		V
$V_{CM}$	Input Common-Mode Voltage Range		$\pm 16$	+19.5		$\pm 11$	+14.5		V
				-16.5			-11.5		V
CMRR	Common-Mode Rejection Ratio	$R_S \leq 10\text{k}$	80	100		70	100		dB
PSRR	Supply Voltage Rejection Ratio	(Note 7)	80	100		70	100		dB
$I_S$	Supply Current			1.8	2.8		1.8	3.4	mA

## AC Electrical Characteristics (Note 4)

Symbol	Parameter	Conditions	LF411A			LF411			Units
			Min	Typ	Max	Min	Typ	Max	
SR	Slew Rate	$V_S = \pm 15\text{V}$ , $T_A = 25^\circ\text{C}$	10	15		8	15		$\text{V}/\mu\text{s}$
GBW	Gain-Bandwidth Product	$V_S = \pm 15\text{V}$ , $T_A = 25^\circ\text{C}$	3	4		2.7	4		MHz

# CARACTERISTICAS ELECTRICAS AMP-OP-TL081

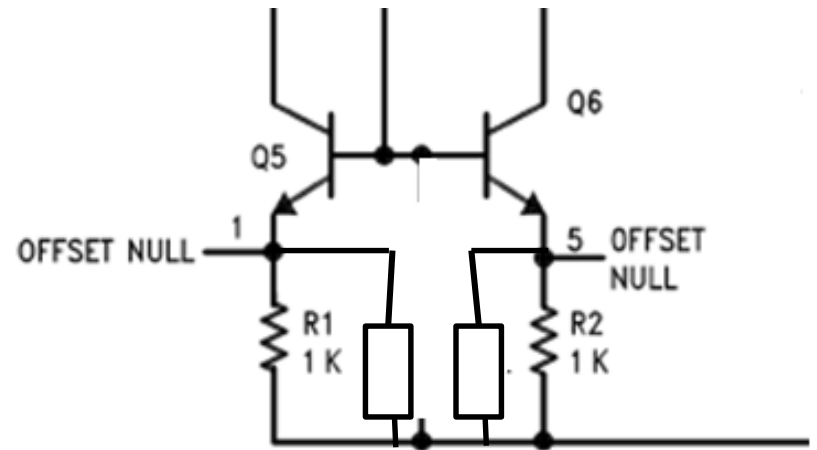
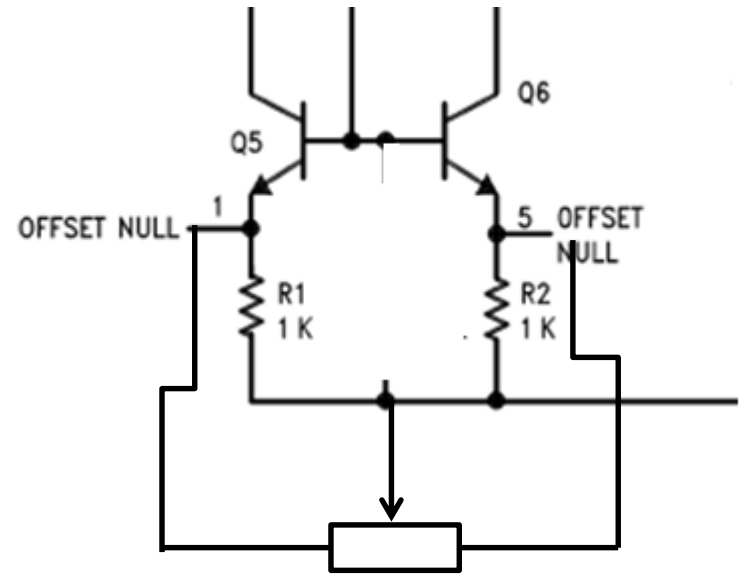
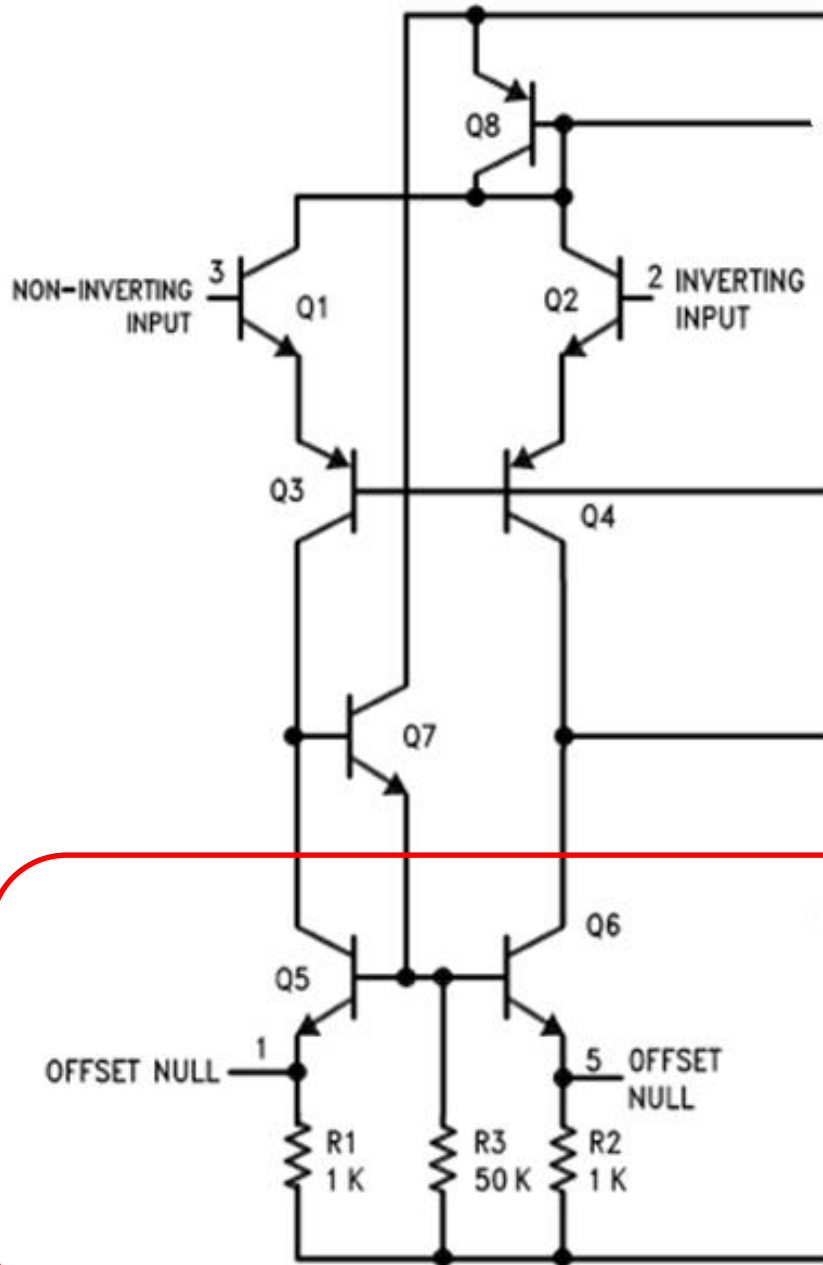
## DC Electrical Characteristics (Note 3)

Symbol	Parameter	Conditions	TL081C			Units
			Min	Typ	Max	
$V_{OS}$	Input Offset Voltage	$R_S = 10\text{ k}\Omega$ , $T_A = 25^\circ\text{C}$ Over Temperature		5	15 20	mV mV
$\Delta V_{OS}/\Delta T$	Average TC of Input Offset Voltage	$R_S = 10\text{ k}\Omega$		10		$\mu\text{V}/^\circ\text{C}$
$I_{OS}$	Input Offset Current	$T_j = 25^\circ\text{C}$ , (Notes 3, 4) $T_j \leq 70^\circ\text{C}$		25	100 4	pA nA
$I_B$	Input Bias Current	$T_j = 25^\circ\text{C}$ , (Notes 3, 4) $T_j \leq 70^\circ\text{C}$		50	200 8	pA nA
$R_{IN}$	Input Resistance	$T_j = 25^\circ\text{C}$		$10^{12}$		$\Omega$
$A_{VOL}$	Large Signal Voltage Gain	$V_S = \pm 15\text{V}$ , $T_A = 25^\circ\text{C}$ $V_O = \pm 10\text{V}$ , $R_L = 2\text{ k}\Omega$ Over Temperature	25 15	100		V/mV V/mV
$V_O$	Output Voltage Swing	$V_S = \pm 15\text{V}$ , $R_L = 10\text{ k}\Omega$	$\pm 12$	$\pm 13.5$		V
$V_{CM}$	Input Common-Mode Voltage Range	$V_S = \pm 15\text{V}$	$\pm 11$	+ 15 - 12		V V
CMRR	Common-Mode Rejection Ratio	$R_S \leq 10\text{ k}\Omega$	70	100		dB
PSRR	Supply Voltage Rejection Ratio	(Note 5)	70	100		dB
$I_S$	Supply Current			1.8	2.8	mA

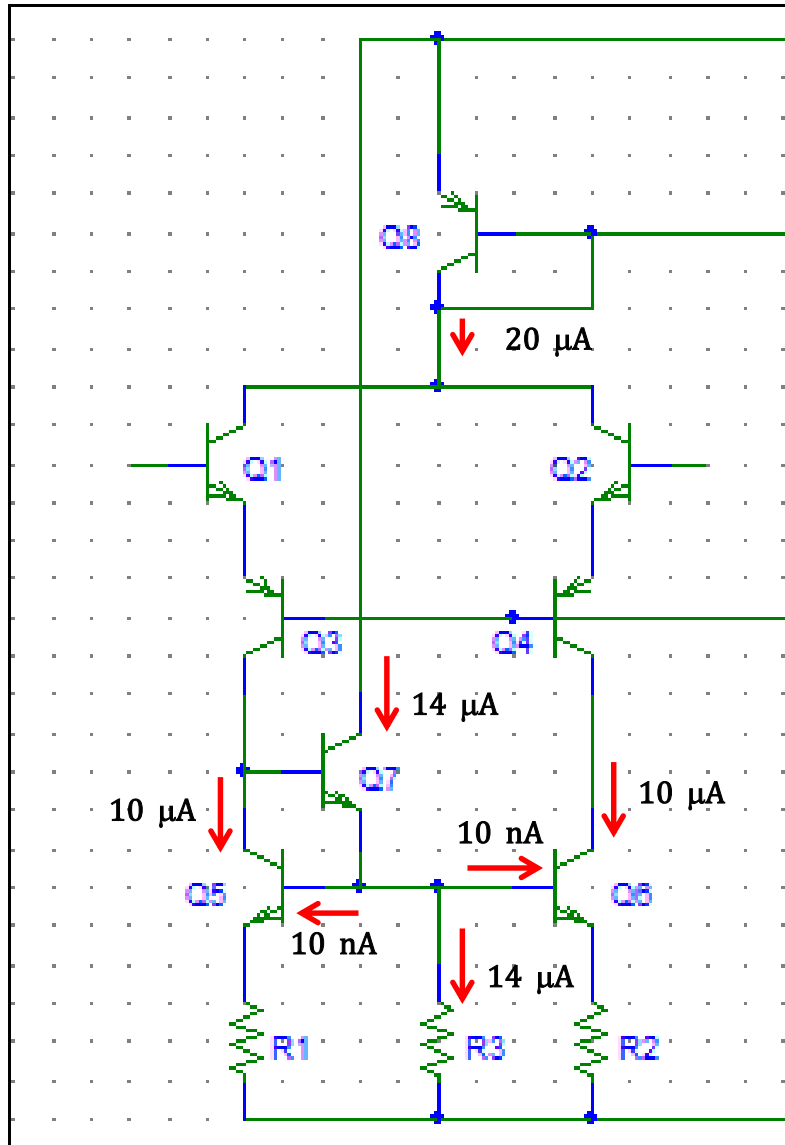
## AC Electrical Characteristics (Note 3)

Symbol	Parameter	Conditions	TL081C			Units
			Min	Typ	Max	
SR	Slew Rate	$V_S = \pm 15\text{V}$ , $T_A = 25^\circ\text{C}$		13		V/ $\mu\text{s}$
GBW	Gain Bandwidth Product	$V_S = \pm 15\text{V}$ , $T_A = 25^\circ\text{C}$		4		MHz

# XX741 COMPENSACION OFF-SET



# Polarización de Q<sub>7</sub>

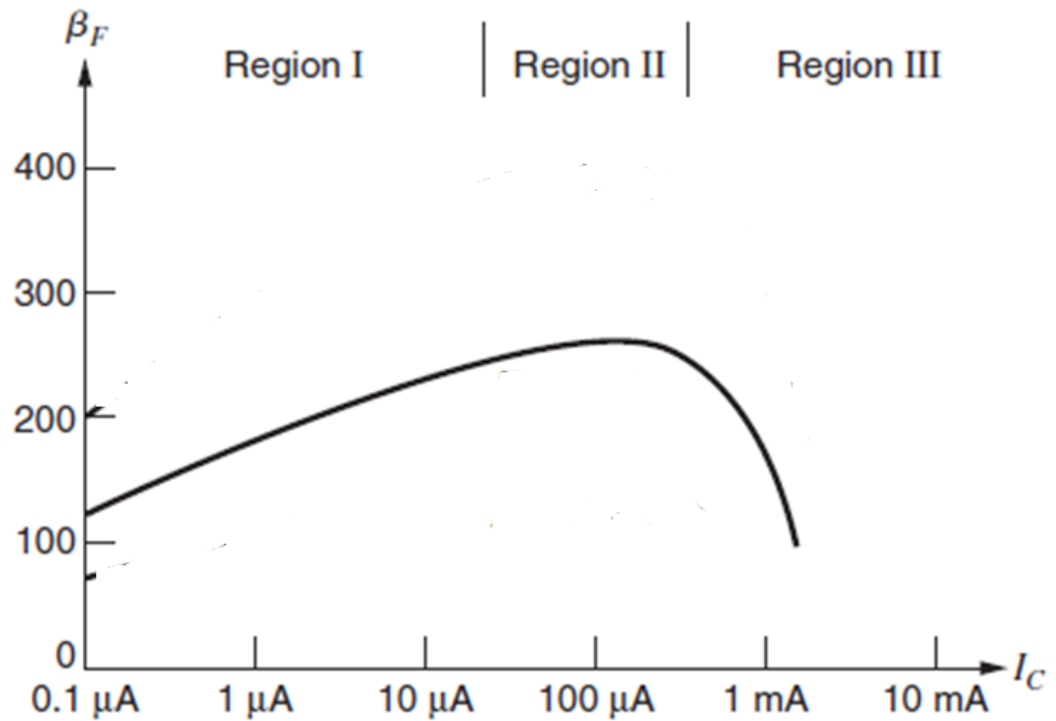


$$V_{R3} = V_{BE1,2} + 10\mu A \times R_{1,2}$$

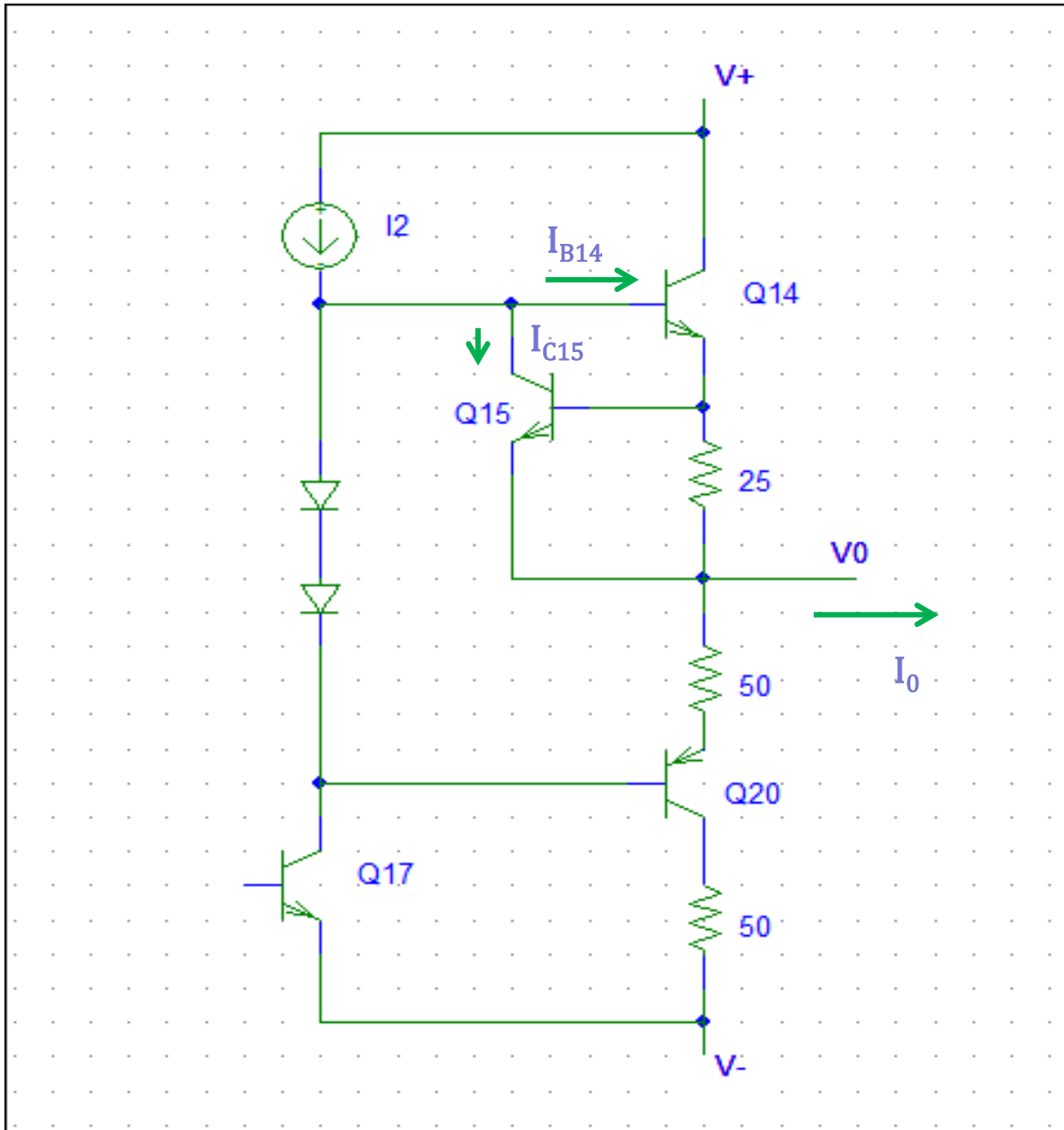
$$V_{R3} = 0.7 V$$

$$I_{R3} = \frac{V_{R3}}{50 K\Omega} = 14 \mu A$$

$$I_{R3} \approx I_{C7}$$



# XX741 PROTECCION



$$V_{BE15} = I_0 \times 25$$

$$I_0 \times 25 \cong 0.7 \text{ V}$$

$$I_{C15} \neq 0$$

Disminuye  $I_{B14}$